

ABSTRACT OF THE DISCLOSURE

5 A non-traditional, non-uniformity compensation processor. In the illustrative embodiment, the feedforward adaptive non-uniformity compensation processor includes a multiplier which receives a video image and generates a compensated video image by multiplying the video signal by one of a noise reducing constant or one. The inventive processor further includes a shunting multiplication processor which supplies a selected
10 one of the noise reducing constant or one to the multiplier in response to the presence of one of fixed pattern noise (FPN) and temporal noise (TN) in adjacent frames of the video image. In an illustrative embodiment, the inventive system cones the output of an FPA to generate a coned output signal. The system isolates the FPN in the coned output signal and feedforward processes the coned output signal on a pixel-by-pixel basis to generate a
15 FPN-reduced output signal. In an exemplary embodiment, the feedforward processing step includes the step of multiplying the coned output signal by a predetermined value which is substantially smaller, i.e., approximately zero, than the coned output signal.

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